



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)

Munaf Rahimo et al.)

Group Art Unit: 2811

Application No.: 10/537,834)

Examiner:

Filed: June 8, 2005)

Confirmation No.: 8689

For: NEW SELF-ALIGNED PRODUCTION)
METHOD FOR AN INSULATED GATE)
SEMICONDUCTOR DEVICE CELL)
AND INSULATED GATE)
SEMICONDUCTOR DEVICE CELL)

REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT

Commissioner for Patents
Office of Initial Patent Examination
Customer Service Center
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Enclosed is a copy of the Official Filing Receipt marked in red to show correction that is needed. The correction is as follows:

Please change the title **FROM Insulated gate semiconductor device and method of making the same TO NEW SELF-ALIGNED PRODUCTION METHOD FOR AN INSULATED GATE SEMICONDUCTOR DEVICE CELL AND INSULATED GATE SEMICONDUCTOR DEVICE CELL** as amended in the Preliminary Amendment filed on June 8, 2005.

Issuance of a corrected Official Filing Receipt is respectfully requested.

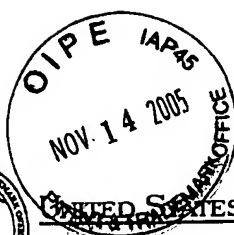
Respectfully submitted,

BUCHANAN INGERSOLL PC

Date: November 14, 2005

By: 
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P.O. Box 1404
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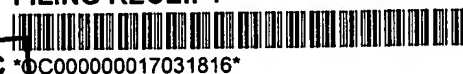
APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/537,834	06/08/2005	2811	900	004501-813	8	3	2

CONFIRMATION NO. 8689

21839

BUCHANAN INGERSOLL PC
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FILING RECEIPT



0C000000017031816

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SEP 26 2005

Date Mailed: 09/21/2005

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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004501-813

PCL/BTP

Assignment For Published Patent Application

ABB SCHWEIZ AG, baden, SWITZERLAND

Power of Attorney: The patent practitioners associated with Customer Number 21839.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/CH03/00809 12/09/2003

Foreign Applications

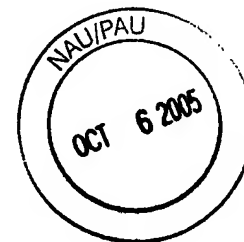
EUROPEAN PATENT OFFICE (EPO) 024060865 12/10/2002

Projected Publication Date: 12/22/2005

Non-Publication Request: No

Early Publication Request: No

Title



~~Insulated gate semiconductor device and method of making the same~~
 New Self-aligned production method for an insulated gate
 Preliminary Class Semiconductor device cell and insulated gate
 257 Semiconductor device cell

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